

AMENDMENTS TO THE CLAIMS:

Without prejudice, this listing of the claims replaces all prior versions and listings of the claims in the present application:

LISTING OF CLAIMS:

1. (Currently Amended) A device for receiving data transmitted using asynchronous data transmission technology, comprising:

 a data-independent clock signal;

 a memory device which stores the received data for the required period of time such that a period between two disturbances is made long so that any effect of the two disturbances is reduced, the memory device having sufficient memory to store a multitude of data and associated clock times; and

 a switchover device is provided, which classifies the received data into data classes and relays them to a corresponding device,

wherein the data-independent clock signal is sent to the memory device for readout of the received data.

2. (Currently Amended) The device according to claim 1, characterized in that the memory device [[it]] is designed for receiving data transmitted by the ATM technology device and is designed as a FIFO memory.

3. (Previously Presented) The device according to claim 1, characterized in that the memory device is designed as a FIFO memory.

4. (Previously Presented) The device according to claim 3, characterized in that the FIFO memory is dimensioned so that the received data are storable for a period of 100 Φs to 250 Φs per switching node.

5. (Previously Presented) The device according to claim 1, characterized in that a clock providing a clock signal is synchronized with at least one other device.

6. (Previously Presented) The device according to claim 1, characterized in that the clock is not synchronized with the clock of the transmitting device, and means for adjusting the received data stream to the clock rate of the clock are provided.

7. (Previously Presented) The device according to claim 6, characterized in that the clock rate adjusting means effect one of doubles and omits certain data signals when reading from the memory device.

8. (Previously Presented) The device according to claim 1, characterized in that the clock is synchronized via an external normal clock rate.

9. to 17. (Canceled)

18. (Previously Presented) The device according to claim 1, wherein the asynchronous data transmission technology is at least one of audio data and video data.